
DTC messages

The following is a brief discussion of the messages relating to DTC. For a complete discussion, refer to *X11 input/output guide* (553-3001-400).

DTC001	Clock Controller tracking on primary source loop.
DTC002	Clock Controller tracking on secondary source loop.
DTC003	Clock Controller cannot be accessed.
DTC004	Clock Controller indicates clock aging error (not locked on in 5 seconds).
DTC005	Reference clock switched to secondary source from primary.
DTC006	Reference clock switched to free run mode from secondary or primary.
DTC007	Active reference clock is set to retrack primary.
DTC008	Active reference is free run or the Clock Controller cannot be accessed.
DTC009	Clock controller has been switched.
DTC010	UART error is detected. Active CC cannot contact other CC.
DTC011	Clock Controller self-test failed; error exists.
DTC012	Clock Controller has reference clock problem.
DTC013	Clock Controller has tracking problem.
DTC014	Clock Controller set to free run.
DTC015	Clock Controller set to secondary.
DTC016	Clock Controller restored from free run or secondary to tracking on primary.
DTC017	Clock Controller restored from free run to tracking on secondary.
DTC018	Cannot switch or restore to a reference clock because automatic reference clock switching option is disabled.
DTC100	An invalid error message was received from the Clock Controller.
DTC101	You can only disable the secondary clock reference.
DTC102	You can only enable the secondary clock reference.
DTC103	System is locked to primary clock reference.
DTC104	System is locked to secondary clock reference.

DTC105	System is locked to free run.
DTC106	Supposed to free run but hardware is tracking to primary.
DTC107	Supposed to free run but hardware is tracking to secondary.
DTC108	Supposed to track on primary but hardware is tracking to secondary.
DTC109	Supposed to track on secondary but hardware is locked to primary reference clock.
DTC110	Supposed to track on secondary but hardware is tracking to primary.